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of rules that describe the  
capabilities and

programming model of  
a computer. The subject  
includes Machine

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Data path, and  
Architecture  
control unit, Instruction  
pipelining, Memory  
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storage, and I/O  
interface (Interrupt and  
DMA mode) with a  
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the modification only  
affects the multiply  
instruction, then it can  
be assumed that IC and

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C remains the same in both situations. In order to find out N it is necessary to write the CPU\_time for both ...

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## Cache Memory...

### 3. Data Transfer Rate-

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The time taken to transfer the data is called as transfer time. It depends on the following factors-

Number of bytes to be transferred. Rotation

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