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the PCIe endpoint mode:

1. Connect the heat sink fan power line to J18.
2. Plug C29x PCIe into the PCIe slot on the motherboard.

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steps to use the C29x PCIe card in the PCIe endpoint mode: 1.

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configurations. C29x PCIe Card User Guide, Rev. 1, 03/2014 Freescale Semiconductor, Inc. 13

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1. Navigate to

`./software/user/example`
under the design example
directory. 2. Compile the
design example

application: `$ make` 3.

Run the test: `$./intel_fpga_pcie_link_test`
You can

run the Intel FPGA IP

PCIe link test in manual

or automatic mode. —

In automatic mode, the
application automatically

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selects the device. The
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About This Guide This user guide describes the function and operation of the Spartan®-6 FPGA Integrated Endpoint Block for PCI Express® core, including how to

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design, customize, and implement the core.

Guide Contents This manual contains these chapters and appendices:

† Chapter 1, Introduction, describes the core and related information, including

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Adapter HW USER GUIDE - Adaptive Modules

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x1, x2, x4, x8, or x16 configurations. Figure 1.
Stratix 10 PCIe Variant
with Avalon-ST Interface

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Avalon-ST Interface for
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Procedure 1. Align the

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full-length mini-PCIe module with the full-length slot on the mini-PCIe adapter board. 2. Insert the mini-PCIe module connector into the slot. 3. Using a Phillips screwdriver, insert and tighten the two screws to secure the mini-PCIe module to the board. Page 33: Configuration

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